

**ARCHITECTED REGISTER FILE EXTENSION IN A MULTI-THREAD PROCESSOR****5    ABSTRACT**

An apparatus, a method, and a computer program are provided for an architected register file system for multithread system. In conventional architected register file systems, a thread is only capable of utilizing a single 10 register file. However, when register files of other thread are unused, the system resources are wasted. In the modified architected register file system, though, threads are enabled to utilize register files of other threads. The utilization of other thread registers is through the use of 15 control fields added to a Status and Control Register (SCR) associated with each register file that enable and disable usage of other register files.